



Learn. Explore. Excel.

• Hyderabad • Guntur

CAREER READINESS PROGRAMME IN VLSI ENGINEERING

Entrance test - Preliminary (online)	Approved candidates with Hall tickets	11/12/21, 10 AM to 11 AM
Entrance test - Main (offline)	Short-Listed candidates in Prelim.	19/12/21, 10 AM to 12 Noon
Interview (offline)	Short-Listed candidates in Main	From 21/12/21 onwards

VLSI Digital Engineering

- Logic Design/ Verification/ Implementation
- Physical Design/ Verification

VLSI Analog Engineering

- Analog Design
- Custom Layout Design

- 6 months Full-time Intensive and Industry oriented training.
- Course Fee: Depends on the course.
- VEDA IIT will facilitate / guide placement in the associated and other companies for candidates successful in the program.

About VEDA

- Expert faculty from reputed VLSI & Embedded Design Companies/ Institutes.
- Well-equipped Labs with high-end, state-of-the-art computing facilities.
- Excellent placement track record in leading technology companies since inception.
- VEDA trained engineers are equipped with the required skills to work on state-of-the-art technology solutions, collaboratively with global technology teams.
- Listed among top five institutes in VLSI Engineering by Times of India.
- Innovative Teaching Company with Industry and University participation - First of its kind.
- Pioneered in incubating many successful design teams/ companies.
- Lead organizers of International VLSI & Embedded System Design Conferences VLSI 2006, 2008, 2012 & 2017.

ELIGIBILITY

- B.Tech./M.Sc. in Electronics/ Electrical/ Instrumentation
- Final year students can also apply provided they can attend full-time training program.

APPLY AT WWW.VEDAIIT.ORG

For course fee details, please see the admission brochure in the website

Registration Fee : Rs. 500/-

Last date for application: 08th December 2021



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VEDA IIT, A Unit of The VEDA Educational Society

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