

Junior Engineer Trainees Recruitment

Challenging opportunities for Polytechnic Diploma Holders

Entrance test - Preliminary (online)	Approved candidates with Hall tickets	30/04/22, 10 AM to 11 AM
Entrance test - Main (offline)	Short-Listed candidates in Prelim.	07/05/22, 10 AM to 1 PM
Interview (offline)	Short-Listed candidates in Main	From 09/05/22 onwards

VLSI Design & Engineering

Logic Design

RTL Design, Implementation, Verification, DFT & Emulation

Physical Design(PD)/Verification(PV)

PD: Physical Placement & Routing, Functional Equivalence, Timing Closure.
PV: Layout, LVS and Design Rule Check

ELIGIBILITY:

Diploma in Electronics/ Electrical/ Communications/ IE or B.Sc. in Electronics

Embedded System Design & Engineering

Embedded Software Development

Firmware/ Drivers/ Multimedia SW for Embedded Devices, Systems & Solutions

Software testing

Automation, Testing & QA for Embedded SW, Systems & Applications

Application SW Development

Mobile applications (Android, IOS), web applications (front-end, back-end), desktop tools & applications

System Engineering

Package design, HW/Board Design
Pre & Post Silicon Validation/Characterization

ELIGIBILITY:

Diploma in Electronics/ Electrical/ Communications/ IE/ Computer Science/ Instrumentation or B.Sc. in Electronics/ Computer Science or BCA

About VEDA

- Expert faculty from reputed VLSI & Embedded System Design Companies/ Institutes
- Well-equipped Labs with high-end, state-of-the-art computing facilities
- VEDA trained engineers are equipped with the required skills to work on state-of-the-art technology solutions, collaborating with global technology teams
- Innovative Teaching Company with Industry and University participation - First of its kind. Pioneered in incubating many successful design teams/ companies
- One of the top five institutes in VLSI Engineering as ranked by Times of India
- Received laurels as the best industry-oriented training institute in VLSI and Embedded System design from global design companies

About SoCtronics:

- Customer-focused VLSI and Embedded system design services company
- Offers spec-to-system turn-key solutions involving all aspects of chip design and embedded software
- Key industries: Consumer electronics, IoT, Automotive, Medical, Defense, Low power computing, graphics
- Work on cutting edge technologies involving latest technology nodes, multimedia standards, wireless & networking protocols, application frameworks & stacks and AI/ML solutions
- Work with top-tier semiconductor and system companies across the world

6 months Company sponsored
knowledge-intensive, industry oriented full-time training at VEDA IIT
(with stipend payment)

APPLY AT www.vedaiit.org

Registration fee for Pass-outs: Rs. 300/-
Last date for application : **25th April 2022**

No fees for campus final year students who apply on-line with 'valid coupon code'* by **25th April 2022**

*VEDA IIT provides a limited number of 'valid coupon codes' to TPO/HODs of different colleges on request to hr@vedaiit.com