

## Junior Engineer Trainees Recruitment

Challenging opportunities for Polytechnic Diploma holders

**Entrance Test** (Offline)

Approved candidates with Hall tickets

08/01/23, 10 AM to 1 PM

**Interview** (Offline)

Short-listed candidates in Entrance test

From 11/01/23 onwards

### VLSI Engineering

#### Physical Design Verification(PV)

Layout Versus Schematic & Design Rule checks, Manufacturability and Yield checks, Circuit electrical checks using advanced EDA tools

#### ELIGIBILITY:

Diploma in Electronics/ Electrical/ Communications/ IE or B.Sc. in Electronics

### Embedded System Design & Engineering

#### Embedded Software Development

Firmware/ Drivers/ Multimedia SW for Embedded Devices, Systems & Solutions

#### Software Testing

Automation, Testing & QA for Embedded SW, Systems & Applications

#### Application SW Development

Mobile applications (Android, iOS), web applications (front-end, back-end), desktop tools & applications

#### System Engineering

Package design, HW/Board Design, Pre & Post Silicon Validation/Characterization

#### ELIGIBILITY:

Diploma or B.Sc. in Electronics/ Electrical/ Communications/ IE/ Computer Science/ IT/ Embedded Systems/ Instrumentation or BCA

### About VEDA

- Expert faculty from reputed VLSI & Embedded System Design Companies/ Institutes
- Well-equipped Labs with high-end, state-of-the-art computing facilities
- VEDA trained engineers are equipped with the required skills to work on state-of-the-art technology solutions, collaborating with global technology teams
- Innovative Teaching Company with Industry and University participation - First of its kind. Pioneered in incubating many successful design teams/ companies
- One of the top five institutes in VLSI Engineering as ranked by Times of India
- Received laurels as the best industry-oriented training institute in VLSI and Embedded System design from global design companies

### About SoCtronics:

- Customer-focused VLSI and Embedded system design services company
- Offers spec-to-system turn-key solutions involving all aspects of chip design and embedded software
- Key industries: Consumer electronics, IoT, Automotive, Medical, Defense, Low power computing, graphics
- Work on cutting edge technologies involving latest technology nodes, multimedia standards, wireless & networking protocols, application frameworks & stacks and AI/ML solutions
- Work with top-tier semiconductor and system companies across the world

**6 months company sponsored\*\***  
**knowledge-intensive, industry oriented full-time training at VEDA IIT**  
**(with stipend payment)**

\*\* For terms & conditions refer to the hyperlink on JRF at [vedaiit.org](http://vedaiit.org) home page.

**APPLY AT [www.vedaiit.org](http://www.vedaiit.org)**

Registration fee for Pass-outs: Rs. 300/-

Last date for application : **03<sup>rd</sup> Jan 2023**

No fees for campus final year students who apply on-line with 'valid coupon code'\* by **03<sup>rd</sup> Jan 2023**

\*VEDA IIT provides a limited number of 'valid coupon codes' to TPO/HODs of different colleges on request to [hr@vedaiit.com](mailto:hr@vedaiit.com)