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# FACULTY RECRUITMENT

## HYDERABAD

### Asst. Professor

**Job Description:** Teaching, Research & Training  
**Eligibility:** ME/M.Tech/MS/M.Sc/Ph.D in relevant branch with 1st class either in UG or PG  
Teaching Experience in relevant field is desirable  
Salary: Negotiable based on experience and domain

### Engineer/Teaching Associate

**Job Description:** Design of experiments, development & training  
**Eligibility:** BE/B.Tech/M.Sc in Electronics/Electrical/Instrumentation with 1st class

## Domains

### VLSI Analog Engineering

#### Analog Design

Analog / IO / Std. Cell / Memory Circuit Design, Verification

#### Circuit Layout Design

Analog / IO / Std. Cell / Memory Circuit Custom Layout, Layout Verification

The selected candidates will be provided 6 months of knowledge intensive, full-time training followed by internship based on requirements.

ABOUT VEDA



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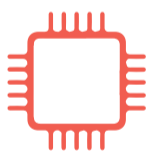
250+ Batches



Well Equipped Labs



Top rated VLSI Institute



Latest EDA Tools



Consortium Companies



MoU with Multiple Universities



**APPLY ONLINE - [www.vedaiit.org](http://www.vedaiit.org)**

Last date to Apply/ Send resumes: **28<sup>th</sup> April, 2026**

Screening Test - Offline : **02<sup>nd</sup> May, 2026**

Interview: Successful candidates in Screening Test will be called for interview

For more details of the Screening Test visit: <https://vedaiit.org/veda-faculty-recruitment/>



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**VEDA IIT, A Unit of The VEDA Educational Society**

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