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FACULTY RECRUITMENT

HYDERABAD & GUNTUR

Asst. Professor

Job Description: Teaching, Research & Training.

Eligibility: ME/M.Tech/MS/M.Sc/Ph.D in relevant branch with 1st class either in UG or PG

Teaching Experience in relevant field is desirable

Salary: Negotiable based on experience and domain.

Engineer/Teaching Associate

Job Description: Design of experiments, development & training.

Eligibility: BE/B.Tech/M.Sc in Electronics/Electrical/Computer Science/IT/Instrumentation/MCA with 1 st class.

Salary: Rs. 6 to 9 Lakhs.

Domains

VLSI Analog Engineering

Analog Design

Analog Circuit Design, Spice Simulation and Verilog A Modelling

Circuit Layout Design

Analog/Custom Circuit Layout/ Layout Verification

The selected candidates will be provided 6 months of knowledge intensive, industry-oriented full-time training based on requirements.

ABOUT VEDA



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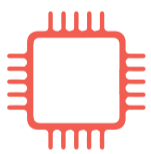
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APPLY ONLINE - www.vedaiit.org

Last date to Apply/ Send resumes: **11th March, 2024**

Screening Test - Offline (On chosen core domain): **16th March, 2024; 10 AM to 1 PM**

Interview: Successful candidates in Screening Test will be called for interview.

For more details of the Screening Test visit: <https://vedaiit.org/veda-faculty-recruitment/>



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