### **GATEWAY TO A LUCRATIVE CAREER IN SEMICONDUCTOR INDUSTRY**



## **VEDA IIT OFFERS**



# INDUSTRY-READY PROFESSIONAL TRAINING IN VLSI DESIGN/EMBEDDED SYSTEM DESIGN

In fulfillment of coursework for 2 Semesters of B.Tech 4<sup>th</sup> Year Anurag University

in collaboration with

Soctronics & Other Consortium Companies

VEDA IIT, one of the top VLSI Engineering Institutes, conducts the program

**Knowledge-intensive and Industry-Oriented Program** 

Anurag University approved transfer of 40 credits earned at VEDA IIT for award of B.Tech Degree Internship in 2<sup>nd</sup> Semester in VEDA IIT/ Consortium company\*

\*Terms and conditions apply. For details contact HR, VEDA IIT

#### **COMMON ENTRANCE TEST**

- > Preliminary Test (Online): 6<sup>th</sup> June, 2025; 10:30 AM 11:30 AM
- Main Test (Offline): 15<sup>th</sup>June, 2025; 10 AM − 1 PM Test Center: VEDA IIT, Hyderabad Last Date to apply: 31<sup>st</sup>May, 2025

#### **ELIGIBILITY & ADMISSION**

- ➤ Students of Anurag University, completing
  B.Tech. III year by 11<sup>th</sup> July, 2025 in ECE/EEE/EIE/CSE
- Admission: Based on performance in the entrance tests, Interview and NOC issued by the colleges / institutes

For more information and to apply online Visit: <u>www.vedaiit.org</u>

Registration fee: Rs 300/-

No fees for campus students who apply on-line with valid coupon code

VEDA IIT provides a limited number of 'valid coupon codes' to TPO/HODs of different colleges on request to hr@vedaiit.org

#### **ABOUT VEDA IIT**

- Expert faculty from reputed VLSI Design Houses/Institutes
- Projects with proven design methodologies
- Well-equipped infrastructure with high-end state-of-the-art computing facilities
- Excellent placement track record in leading Technology Companies since inception
- Partnership with global leaders of technology

- Innovative Teaching-Company model with Industry and University participation - first of its kind
- Listed among top five institutes in VLSI Engineering by Times of India
- Pioneered in incubating many successful design teams/companies
- Lead organizers of International VLSI Conferences -VLSI 2006, 2008, 2012 & 2017
- Holistic education covering various aspects of VLSI and System Design

Hands-on Experience with best-in-class tools covering the entire ASIC design flow from the world class EDA Vendors

cādence™

**SIEMENS** 





VEDA IIT, A Unit of The VEDA Educational Society

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