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ACCREDITED BY NAAC



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

(Established by Govt. Act No. 30 of 2008)

Kukatpally, Hyderabad – 500 085, Telangana (India).

Dr.K. VENKATESWARA RAO

M. Sc.,M.Tech.,Phd.,PDF(Johns Hopkins Medicine USA)
Professor of Nano Technology &

REGISTRAR

Lr. No. JNTUH/DAAF/A3/1510/2024

Date.06.07.2024

To

The Principals of the all JNTUH University Colleges,
The Principals of the all Autonomous and Affiliated Colleges of JNTUH.

Sub: JNTUH-DAAF – Permission for offer Industry Ready Professional Training in VLSI Design/Embedded System Design course to B. Tech. – ECE/EEE/EIE/CSE IV Year I & II Semester for the Academic Year 2024-25.

Ref: 1. Circular No. JNTUH/DAP/A3/1510/2022 Dt. 20.07.2022.

2. Letter dated. 26.06.2024 of the Director & Head, VEDA IIT, Hyderabad.

3. University Lr. No. JNTUH/DAP/A3/1510/MoU/VEDA IIT/22, dated.30.12.2022.

4. Note orders of the Vice-Chancellor *l/c.*, dated. 04.07.2024.

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With reference the letter and e-mail (1&2) cited above, it is to inform you that the Hon'ble Vice-Chancellor *l/c.* is pleased to accord permission to offer 'Industry Ready Professional Training' programme in VLSI Design / Embedded System Design to B. Tech. - ECE/EEE/EIE/CSE IV Year I & II Semester. The students of B. Tech. - ECE/EEE/EIE/CSE IV Year I & II Semester can opt for the courses offered by VEDA IIT, Hyderabad in collaboration with Consortium of Industries viz., M/s. SoCronics Technologies Private Limited. The courses offered by the JNTUH University, Autonomous & Affiliated Colleges in IV Year B. Tech. I & II Semesters for 40 (20+20) credits suitably modified with the courses offered by VEDA IIT, Hyderabad for 40 credits to make the students industry ready in the area of VLSI Design or Embedded System Design for B. Tech. - ECE/EEE/EIE/CSE IV Year I & II Semester students for the Academic Year 2024-25.

Hence, the Principals of JNTUH University, Autonomous and Affiliated Colleges are requested to inform to the students and submit Expression of Interest (EoI) indicating the list of students (with name & H.T No.) willing to opt. IV Year -I & II semester courses in area of VLSI & Embedded Systems offered at VEDA Educational Society (VEDA IIT), Aydiv IT Park, Hyderabad on or before 22.07.2024 to the Registrar, JNTUH and simultaneously register in www.vedaiit.org.

Yours sincerely,


REGISTRAR

Encl: Admission Notification, Approved course structure IV Year I and II semester procedure for conducting of internal examination and Semester End Examination.

Copy to:

The Director, University Examinations (DUEX), JNTUH,

The Controller of Examination, JNTUH.

PA to VC/Rector/Registrar for information.