

GATEWAY TO A LUCRATIVE CAREER IN TECHNOLOGY COMPANIES



VEDA IIT OFFERS



INDUSTRY-READY PROFESSIONAL TRAINING IN

VLSI DIGITAL/ANALOG ENGINEERING, EMBEDDED SYSTEM DESIGN & APPLIED AI

In fulfillment of coursework for 2 Semesters of B.Tech 4th Year

for JNTUK which allow 2 Semesters of Industry / Institute Training / Internship on full-time basis

in collaboration with

Oraina atai & Other Consortium Companies

VEDA IIT, one of the top VLSI Engineering Institutes, conducts the program

Knowledge-intensive and Industry-Oriented Program

JNTUK should approve transfer of 40 credits earned at VEDA IIT for award of B.Tech Degree

Internship in 2nd Semester in VEDA IIT/Consortium company* leading to Employment

*Terms and conditions apply. For details contact HR, VEDA IIT

COMMON ENTRANCE TEST

- Date of Exam : 20th June, 2026
- Preliminary Test : 9:00 AM onwards (as per hallticket)
- Main Test : 3 PM to 5 PM
- Test Center: VEDA IIT, Hyderabad; VR Siddhartha Engg. College/ PVP Siddhartha Institute of Technology, Vijayawada.
- Last Date to apply: 17th June, 2026

ELIGIBILITY & ADMISSION

- Students of B.Tech. III year completed with 75% and above in ECE/EEE/EI/CSE/AI & ML
- Admission: Based on performance in the entrance tests, Interview and NOC issued by the colleges / institutes

For more information and to apply online
Visit : www.vedaiit.org

Registration fee : Rs 300/-

No fees for campus students who apply on-line with valid coupon code

VEDA IIT provides a limited number of 'valid coupon codes' to TPO/HODs of different colleges on request to hr@vedaiit.org

ABOUT VEDA IIT

- Expert faculty from reputed VLSI Design Houses/Institutes
- Projects with proven design methodologies
- Well-equipped infrastructure with high-end state-of-the-art computing facilities
- Excellent placement track record in leading Technology Companies since inception
- Partnership with global leaders of technology
- Innovative Teaching-Company model with Industry and University participation - first of its kind
- Listed among top five institutes in VLSI Engineering by Times of India
- Pioneered in incubating many successful design teams/companies
- Lead organizers of International VLSI Conferences - VLSI 2006, 2008, 2012 & 2017
- Holistic education covering various aspects of VLSI and System Design

Hands-on Experience with best-in-class tools covering the entire ASIC design flow from the world class EDA Vendors

cādence™

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Silicon to Software



VEDA IIT, A Unit of The VEDA Educational Society

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