

GATEWAY TO A LUCRATIVE CAREER IN SEMICONDUCTOR INDUSTRY



VEDA IIT OFFERS



INTEGRATED DUAL DEGREE PROGRAMS (IDP) VLSI ENGINEERING EMBEDDED SYSTEM DESIGN

(JNTUH approved & Consortium sponsored)

Leading to a prospective job in consortium/associated companies
with a facility to transfer credits earned at VEDA IIT to JNTUH for the award of B.Tech & M.Tech degrees
for B.Tech students who completed 3rd year in JNTUH Constituent, Affiliated and Autonomous Colleges

in collaboration with



VEDA IIT, one of the top VLSI Engineering Institutes, conducts the program

Knowledge-intensive and Industry-Oriented Program

JNTUH approved credits transfer for 4th year of B.Tech and additional 5th year for M.Tech

Internship with Stipend in 5th year and subsequent employment in consortium company*

*Terms and conditions apply. For details contact HR, VEDA IIT

COMMON ENTRANCE TEST

Preliminary Test: 23rd July, 2023; 9:30 AM - 10:30 AM

Main Test: 23rd July, 2023; 2:00 PM - 4:00 PM

Test Center for Preliminary & Main: VEDA IIT, Hyderabad

Last Date to apply: 22nd July, 2023; 1:00 PM

Syllabus: visit www.vedaiit.org/idp

Apply online at www.vedaiit.org

ELIGIBILITY & ADMISSION

- Students of JNTUH Constituent, Autonomous & Affiliated colleges, completing B.Tech. III year by 24th July, 2023 in ECE/EEE/EIE/CSE
- Those who have taken our main test on 4th June 2023 can apply but need not take the test again, as their performance in that test will be considered for the selection
- Admission: Based on performance in the entrance tests & interview
- Program is likely to commence from 25th July, 2023

ABOUT VEDA IIT

- Expert faculty from reputed VLSI Design Houses/Institutes
- Industry projects with proven design methodologies at cutting edge technologies
- Well-equipped infrastructure with high-end state-of-the-art computing facilities
- Excellent placement track record in leading Technology Companies since inception
- Partnership with global leaders of technology
- Innovative Teaching-Company model with Industry and University participation - first of its kind
- Listed among top five institutes in VLSI Engineering by Times of India
- Pioneered in incubating many successful design teams/companies
- Lead organizers of International VLSI Conferences - VLSI 2006, 2008, 2012 & 2017
- Holistic education covering various aspects of VLSI and System Design

Hands-on Experience with best-in-class tools covering the entire ASIC design flow from the world class EDA Vendors



VEDA IIT, A Unit of The VEDA Educational Society

Regd. No. 410/2014 | hr@vedaiit.org
Telangana: 2nd Floor, AYDIV IT Park, Puppallaguda, Rangareddy 500 032
Andhra Pradesh: Vidyanagar, 1st Lane & 1st Cross, Guntur 522 007
HYD: 040 - 43929999 | +91 9989928276 | GNT: 0863 - 2374000 | +91 8328535061