

BECOME A VLSI PROFESSIONAL EVEN BEFORE YOUR FIRST JOB



VEDA IIT OFFERS

Company sponsored

MS IN VLSI Engineering

(University-Industry Partnership Program)

at Hyderabad | at Guntur

under MoU with JNTUH | under MoU with JNTUK

FULL-TIME - 4 SEMESTERS

in collaboration with

SoCtronics

&

invecas



Award of Degree by JNTUH for VEDA IIT-Hyderabad, by JNTUK for VEDA IIT-Amaravati

VEDA IIT, one of the top industry-centric VLSI Engineering Institutes, conducts the program

Knowledge-intensive and Industry-Oriented Program

Course fee sponsored by the consortium company*

2 semesters of internship and subsequent employment in consortium company*

*Terms and conditions apply. For details contact HR, VEDA IIT

COMMON ENTRANCE TEST

Date: 06th Aug, 2022 ; 10:00 AM -1:00 PM

Exam Centers: Hyderabad & Guntur

Online registration fee: Rs 500/-

Last Date: 03rd Aug, 2022

Apply online at www.vedaiit.org

ELIGIBILITY & ADMISSION

- B.E/ B.Tech in Electronics/Electrical/Computer Science/ IT/Instrumentation (should have completed B.Tech degree as on 1st Aug, 2022)
- Admission is based on performance in the entrance test & Interview
- Place of study is based on preference, availability of seats and minimum strength required

ABOUT VEDA IIT

- Expert faculty from reputed VLSI Design Houses/Institutes
- Industry live projects with proven design methodologies at cutting edge technologies
- Well-equipped infrastructure with high-end state-of-the-art computing facilities
- Excellent placement track record in leading Technology Companies since inception
- Partnership with global leaders of technology
- Innovative Teaching-Company model with Industry and University participation - first of its kind
- Listed among top five institutes in VLSI Engineering by Times of India
- Pioneered in incubating many successful design teams/companies
- Lead organizers of International VLSI Conferences - VLSI 2006, 2008, 2012 & 2017
- Holistic education covering various aspects of VLSI and System Design

Hands-on Experience with best-in-class tools covering the entire ASIC design flow from the world class EDA Vendors

cādence

**Mentor
Graphics**

SYNOPSYS
Silicon to Software



VEDA IIT, A Unit of The VEDA Educational Society

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