

GATEWAY TO A LUCRATIVE CAREER IN TECHNOLOGY COMPANIES



VEDA IIT OFFERS

Knowledge-Intensive and Industry-Oriented

TRAINING AND INTERNSHIP IN

VLSI Digital Engineering

(RTL / DV / Implementation / DFT / Physical Design)

VLSI Analog Engineering

(Analog / IO / Std.Cell / Memory / Package Design, IP Build & Validation, Circuit / Package Layout)

Embedded System Design

(Embedded SW Development / Testing, Application SW Development)

Up to 6 months of Internship with stipend after successful completion of 6-month Training leading to sponsorship and employment by the Participating Companies[#]

Applied AI Engineering

(Machine learning & Computer vision, Full stack Application SW)

Direct employment by the Participating Companies[#] after successful completion of 6-month Training

#Subject to their terms and conditions

COMMON ENTRANCE TEST

- ▷ Date Of Exam: 26th, 27th Sep, 2026 (Tentative Dates)*
- ▷ Preliminary Test: 9:00 AM Onwards (Duration: 1 Hour)
- ▷ Main Test: 3 PM to 5 PM (Duration: 2 Hours)
- ▷ Exam Centre: VEDA IIT, Hyderabad.
- ▷ Apply online at www.vedaiit.org
- ▷ Last Date: 22nd September, 2026
- ▷ Registration fee: Rs. 500/-

* Exact date & venue of test will be intimated in the hall ticket

ELIGIBILITY & ADMISSION

- ▷ B.Tech/M.Tech in relevant branches as applicable:
Electronics/VLSI/Electrical/Microelectronics/Instrumentation/
CSE/AI ML/AI&DS/IT; Year Of Passing **2024/2025/2026/2027**
- ▷ Admission: Based on performance in the entrance test & interview.
- ▷ **Note:** TPOs and HODs can secure limited registration fee waiver codes by contacting hr@vedaiit.org. This applies to final-year, second-semester students ready to join the full-time program commencing in Dec 2026/Jan 2027 without interruption.

ABOUT VEDA IIT

- Expert faculty and experienced engineers from reputed VLSI Design and AI R&D Companies.
- Exposure to proven design methodologies in cutting-edge technologies and projects
- Well-equipped infrastructure with high-end state-of-the-art computing facilities
- Outstanding placement track record in leading technology companies since inception
- Partnership with Consortium Companies in VLSI Design & Applied AI
- Innovative Teaching-Company model with Industry and University participation - first of its kind
- Listed among top five institutes in VLSI Engineering by Times of India
- Pioneered in incubating many successful design teams/companies
- Holistic education covering various aspects of VLSI and AI System & Solutions Design

Participating Companies -Industry Support

Oraina

atai

World-class EDA Vendors - Best-in-class EDA Tools

cadence[®] SIEMENS synopsys[®]



VEDA IIT, A Unit of The VEDA Educational Society

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