

GATEWAY TO A LUCRATIVE CAREER IN SEMICONDUCTOR INDUSTRY



VEDA IIT OFFERS

Knowledge-intensive and Industry-oriented

TRAINING AND INTERNSHIP IN VLSI DESIGN/EMBEDDED SYSTEM DESIGN

Up to 6 months of Internship with stipend after successful completion of 6 months Training leading to sponsorship and employment by the Participating Companies#

#Subject to their terms and conditions

COMMON ENTRANCE TEST

Preliminary Test(Online): 18th Oct, 2024; 10 AM - 11 AM

Main Test(Offline): 27th Oct, 2024; 9 AM – 12 NOON

Exam Center: Hyderabad

Last Date: 14th Oct, 2024

Syllabus: Visit www.vedaiit.org

Apply online at www.vedaiit.org

Registration fee: Rs. 500/-

ELIGIBILITY & ADMISSION

- B.Tech./M.Tech. in Electrical/Electronics/VLSI/ Microelectronics/Instrumentation/Computer Science
Note: Limited coupon codes for registration fee waiver will be provided to TPO/HODs upon request to hr@vedaiit.org, provided their final year students can join the full-time program commencing from December 02, 2024.
- Admission: Based on performance in the entrance tests and interview

ABOUT VEDA IIT

- Expert faculty and experienced engineers from reputed VLSI Design Houses/Institutes
- Exposure to proven design methodologies at cutting edge technologies and projects
- Well-equipped infrastructure with high-end state-of-the-art computing facilities
- Outstanding placement track record in leading technology companies since inception
- Partnership with global leaders of technology
- Innovative Teaching-Company model with Industry and University participation - first of its kind
- Listed among top five institutes in VLSI Engineering by Times of India
- Pioneered in incubating many successful design teams/companies
- Lead organizers of International VLSI Conferences - VLSI 2006, 2008, 2012 & 2017
- Holistic education covering various aspects of VLSI and System Design

Hands-on Experience with best-in-class tools covering the entire ASIC design flow from world class EDA Vendors

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VEDA IIT, A Unit of The VEDA Educational Society

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