VEDA
Learn. Explore. Excel.

WORLD CLASS INSTITUTE

Industry leaders in VLSI, Embedded System Design and AI

CAREER READINESS PROGRAMS IN
VLSI Engineering, Embedded System Design and AI

Introduction

VEDA IIT, a unit of "The VEDA Educational Society" Hyderabad,   is an industry driven world-class institute of excellence providing education and training in the fields of VLSI Engineering (Logic Design, Physical Design, Analog Circuit Design & Custom-layout Design), Embedded System Design and AI & Machine Learning. VEDA IIT, a teaching professional industry, is known for conducting graduates recruitment for the industry and providing corporate sponsored training. It also has an excellent track record offering the MS Program in VLSI Engineering, a collaborative program between a Consortium of VLSI Design Houses and two Universities namely Jawaharlal Nehru Technological University Hyderabad in Telangana State and Jawaharlal Nehru Technological University Kakinada in Andhra Pradesh State, India.   Career Readiness Programs are a new Initiative to cater to the needs of a wide spectrum of companies.

Salient Features

- Associated with a consortium of globally operating Semiconductor, Embedded System Design and AI companies - SoCtronics, INVECAS and ATAI. This Consortium helps in designing and structuring the curricula and in imparting industry-oriented training in contemporary technologies.
- VEDA IIT is Partner of University educational programs supported by Synopsys, Cadence Design Systems, Mentor Graphics/Siemens and Xilinx.
- Conducted various recruitment drives for its Consortium and other companies and provided corporate training for around 3,500 professionals, after inception. Many of them are presently working in leadership roles across the globe in world class semiconductor companies

Career Readiness Programs

1. Duration of the training program: 6 months
2. Fees: Rs.1 Lakh to 2.0 Lakhs depending on the domain
3. List of courses, fees and eligibility criteria:

Sl No	Course	Eligibility*	Duration	Fees(Rs in Lakhs)#
Digital Engineering				
1	Logic Design	B.Tech./M.Sc. in Electronics/ Electrical	6 Months	1.5
2	Physical Design		6 Months	2.0
Analog Engineering				
3	Analog Design	B.Tech./M.Sc. in Electronics/ Electrical	6 Months	1.5
4	Custom Layout Design		6 Months	1.5
Embedded System Design				
5	Embedded SW Development	B.Tech./M.Sc. in Electronics/ Electrical/ Computer Science/ IT/ Instrumentation	6 Months	1.2
6	Application SW Development		6 Months	1.2
7	SW Testing		6 Months	1.0
AI & Machine Learning				
8	AI/ML	B.Tech/ M.Tech in Computer Science Engineering/Electronics.	6 Months	1.5

* Final year students also can apply provided their college permits and the candidate can undergo full-time training.

Taxes are extra as per Govt. rules.

Note: Fee payment schedule – 40% of the prescribed fee + GST by way of DD along with 2 post-dated cheques, each for 30% of the prescribed fee +GST, along with mutually agreed collaterals.

Highlights of the Programs

- Curriculum vetted by Industry, from time to time
- State-of-the-art computing infrastructure
- Industry standard sign-off tools
- Industry proven methodologies
- Industry work environment and practices
- Interactive and student centric learning
- Industry experienced faculty and lab professionals
- Challenging assignments & experiments providing hands-on experience
- Access to latest books and manuals
- Expert career guidance and placement assistance
- Highly competent alumni spread all over the globe in top design companies including MNCs

Entrance Test & Selection criteria

- Eligible candidates will be sent Hall tickets through e-mail at least 2 days before the test.
- All candidates with hall tickets must first go through the preliminary test.
- Short-listed candidates in preliminary test need to take Main test.
- Short-listed candidates in Main test shall attend interviews.
- Based on overall merit, candidates will be selected for the program.

Application and Registration fees

1. APPLY AT WWW.VEDAIIT.ORG
2. Registration Fee: Rs. 500/-

Seat allocation process

- Number of seats available in each course is different and may vary from batch to batch.
- Seat allocation in each domain is purely based on the ranking.
- If the number of qualified candidates in a particular course exceeds the number of seats, remaining qualified candidates can be considered for the next training batch on a merit-cum-priority basis.
- If the number of qualified candidates for a particular course is not meeting minimum required strength, the candidates will be considered for the next batch.

Award of Diploma

Diploma will be awarded on successful completion of the program.

VEDA has internal assessment for evaluating the performance in the training at the end of each module.
50% is the criteria for Pass
60% is the criteria for First Class
75% is the criteria for Distinction

Placement Guidance

- After successful completion of the training, VEDA will facilitate placement guidance in associated companies as well as any other company having requirements for fresh engineers
- Within one year of the training, in case the successful candidates are not placed in any of the companies, VEDA can facilitate the candidate to do internship/ additional training.

VEDA IIT, A Unit of The VEDA Educational Society

Regd. No. 410/2014 | ✉ careers@vedaiit.com

Telangana: 2nd floor, AYDIV Tech Park, Financial District, Gachibowli, Puppalguda, Financial District, Nanakaramguda, Hyderabad-500089

Andhra Pradesh: Vidyanagar, 1st Lane & 1st Cross, Guntur 522 007

HYD: 040 - 43929999 | 📱+91 9989928276 | GNT: 0863 - 2374000 | 📱 +91 8328535061