

## Junior Engineer Trainees Recruitment

### Challenging opportunities for Polytechnic Diploma Holders

#### VLSI Design & Engineering

##### Logic Design

RTL Design, Implementation, Verification, DFT & Emulation

##### Physical Design

Physical Placement & Routing, Functional Equivalence, Timing Closure and Design Rule Check

##### Custom layout Design

Custom layout design of high performance Analog Circuits, Layout verification

#### Embedded System Design & Engineering

##### Embedded Software Development

Firmware/ Drivers/ Multimedia SW for Embedded Devices, Systems & Solutions

##### Software testing

Automation, Testing & QA for Embedded SW, Systems & Applications

##### System Engineering

Platform Engineering, HW/Board Design, Silicon Validation/ Characterization

#### ELIGIBILITY

- Diploma in Electronics/ Electrical/ Communications/ IE/ Computer Science/ Instrumentation
- Students pursuing B.tech after completion of diploma are not eligible for this recruitment drive
- Final year students can also apply.

#### About VEDA

- Expert faculty from reputed VLSI Design Houses/ Institutes.
- Well-equipped Labs with high-end state-of-the-art computing facilities.
- Excellent placement track record in leading Technology Companies since inception.
- Listed among top five institutes in VLSI Engineering by Times of India.
- Innovative Teaching Company with Industry and University participation - First of its kind.
- Pioneered in incubating many successful design teams/ companies.
- Lead organizers of International VLSI & Embedded System Design Conferences VLSI 2006, 2008, 2012 & 2017

**6 months Company sponsored**  
**knowledge-intensive, industry oriented full-time training at VEDA IIT**  
**(with stipend payment)**

**APPLY AT [www.vedaiit.org](http://www.vedaiit.org)**

Registration fee for Pass-outs: Rs. 300/-  
 Last date for application : **26<sup>th</sup> July 2021**  
 No fees for campus students who apply on-line with  
 'valid coupon code'\* by **23<sup>rd</sup> July 2021**

\*VEDA IIT provides limited number of 'valid coupon codes' to TPO/HODs of different colleges on request to [hr@vedaiit.com](mailto:hr@vedaiit.com)

**Prelim (online)**

Approved Candidates with Hall Tickets

01/08/2021 : 10 AM to 11 AM

**Main**

Short-listed Candidates in Prelim

09/08/2021 : 10 AM to 12 Noon

**Interview**

Short-listed Candidates in Main

will be communicated

Pre-placement Talk - 01/08/21, 9:00 AM to 9:30 AM



**VEDA IIT, A Unit of The VEDA Educational Society**

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