

# Diploma Trainees Recruitment

Challenging work opportunities for  
Diploma Engineers

at

SoCtronics

invecas

in core domains

## VLSI Design & Engineering

- **Logic Design**  
RTL Design, Implementation, Verification & DFT
- **Physical Design**  
Physical Placement & Routing,  
Functional Equivalence, Timing Closure  
and Design Rule Check
- **Custom Layout Design**  
Custom layout design of high performance  
Analog Circuits, Layout verification

## Embedded System Design & Engineering

- **Embedded Software Development**  
Firmware/ Drivers/ Multimedia SW/ Applications for  
Mobile, Wearable and IOT Systems
- **Software Testing**  
Automation, Testing & QA for Embedded SW,  
Systems & Applications
- **System Engineering**  
Platform Engineering, HW/Board Design, Silicon  
Validation/ Characterization

**ELIGIBILITY :** Diploma in Electronics/ Communications/ IE/ Computer Science/ Instrumentation.  
Final Year Students can also apply.

**6 months of apprenticeship through company sponsored  
knowledge-intensive, industry-oriented full-time training at VEDA IIT  
(Stipend will be paid during apprenticeship)**

**Apply at [www.vedaiit.org](http://www.vedaiit.org)**

**Registration fee for Pass-outs: Rs. 300/- . Last date for application : 12<sup>th</sup> Dec 2016\***

**No fees for campus students who apply on-line with 'valid coupon code'\*\*\* by 12<sup>th</sup> Dec 2016**

\* Application submission can be closed earlier than this whenever the number of applicants exceeds the actual seating capacity at the exam center

\*\* VEDA IIT provides 'valid coupon codes' to TPO/HODs of different colleges on request; Number will be limited to the seating capacity

Test	Eligibility	Date & Time
Prelim.	Approved Candidates with hall tickets	17/12/16, 9 AM to 10 AM
Main	Short-listed candidates in Prelim	17/12/16, 4 PM to 6 PM
Interview#	Short-listed candidates in Main	18/12/16, 9 AM to 7 PM

# Placement talk for Interview Candidates on 18<sup>th</sup> Dec 2016 from 8 AM to 9 AM

**VEDA**®  
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