

Engineer Trainees Recruitment

Unparalleled Opportunities and Unlimited Possibilities

at

SoCtronics

• Hyderabad • Amaravati • Bangalore • Noida •
in core domains

VLSI Digital Engineering	VLSI Analog Engineering	Embedded System Design & Engineering
<ul style="list-style-type: none"> ➤ Logic Design RTL Design, Implementation, Verification, DFT & Emulation ➤ Physical Design Physical Placement & Routing, Functional Equivalence, Timing Closure and Design Rule Check ➤ Standard Cell Design New Technology Node Analysis, Architecture, Design, Optimization, Library characterization, PPA, Bench Marking 	<ul style="list-style-type: none"> ➤ Analog Design Analog circuit design, Spice simulation and Verilog A modeling ➤ Custom Layout Design Custom layout design of high performance Analog Circuits, Layout verification ➤ Design Characterization Define & execute characterization plan, Perform stress tests, Scripting & Automation 	<ul style="list-style-type: none"> ➤ Embedded Software Development Firmware/ Drivers/ Multimedia SW/ Applications for Mobile, Wearable and IOT Systems ➤ Software Testing Automation, Testing & QA for Embedded SW, Systems & Applications ➤ System Engineering Platform Engineering, HW/Board Design, Silicon Validation/ Characterization

ELIGIBILITY : B.Tech./M.Tech./M.S./M.Sc. in Electronics/ Electrical/ Computer Science/ IT/ Instrumentation. Final Year Students can also apply

6 months of company sponsored knowledge-intensive, industry-oriented full-time training at VEDA IIT
(One year Internship for PG Final year students includes above training)

About VEDA

- Expert faculty from reputed VLSI Design Houses/ Institutes
- Well-equipped Labs with high-end state-of-the-art computing facilities
- Excellent placement track record in leading Technology Companies since inception
- Listed among top five institutes in VLSI Engineering by Times of India
- Innovative Teaching Company with Industry and University participation - First of its kind
- Partnership with global leaders in technology
- Pioneered in incubating many successful design teams/companies
- Associated with GLOBALFOUNDRIES collaborative University programs, exploring designs for silicon fabrication shuttle
- Lead organizers of International VLSI Conferences VLSI 2006, 2008, 2012 & 2017

Apply at www.vedaiit.org

Registration fee for B.Tech /M.Tech Pass-outs: Rs. 300/- . Last date for application : 20th Feb 2018*

No fees for campus students who apply on-line with 'valid coupon code' by 16th Feb 2018**

* Application submission can be closed earlier than this whenever the number of applicants exceeds the actual seating capacity at the exam center

** VEDA IIT provides 'valid coupon codes' to TPO/HODs of different colleges on request; Number will be limited to the seating capacity

Test	Eligibility	Date & Time
Prelim.	Approved candidates with hall tickets	24/02/18, 9 AM to 10 AM
Main	Short-listed candidates in Prelim	24/02/18, 3:30 PM to 5:30 PM
Interview	Short-listed candidates in Main	25/02/18, 8:30 AM to 7 PM [#]

Pre-placement Talk - 24/02/18, 10:15 AM to 10:45 AM

[#] Interview schedule beyond this date will be intimated on that day



VEDA IIT, a unit of The VEDA Educational Society

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