

Engineer Trainees Recruitment

Challenging work opportunities for
Graduating Engineers
at

SoCtronics

invecas

in core domains

VLSI Digital Engineering

> Logic Design

RTL Design, Implementation, Verification & DFT

> Physical Design

Physical Placement & Routing, Functional Equivalence, Timing Closure and Design Rule Check

VLSI Analog Engineering

> Analog Design

Analog circuit design, Spice simulation and Verilog A modeling

> Custom Layout Design

Custom layout design of high performance Analog Circuits, Layout verification

Embedded System Design & Engineering

> Embedded Software Development

Firmware/ Drivers/ Multimedia SW/ Applications for Mobile, Wearable and IOT Systems

> Software Testing

Automation, Testing & QA for Embedded SW, Systems & Applications

> System Engineering

Platform Engineering, HW/Board Design, Silicon Validation/ Characterization

ELIGIBILITY : B.Tech./M.Tech./M.Sc. in Electronics/ Electrical/ Computer Science/ IT/ Instrumentation.
Final Year Students can also apply.

6 months of company sponsored

knowledge-intensive, industry-oriented full-time training at VEDA IIT
(One year Internship for PG students includes above training)

Apply at www.vedaiit.org

Registration fee for B.Tech /M.Tech Pass-outs: Rs. 300/-. Last date for application : 5th Dec 2016*

No fees for campus students who apply on-line with 'valid coupon code'** by 30th Nov 2016

* Application submission can be closed earlier than this whenever the number of applicants exceeds the actual seating capacity at the exam center
** VEDA IIT provides 'valid coupon codes' to TPO/HODs of different colleges on request; Number will be limited to the seating capacity

Test	Eligibility	Date & Time
Prelim.	Approved Candidates with hall tickets	10/12/16, 9 AM to 10 AM
Main	Short-listed candidates in Prelim	10/12/16, 4 PM to 6 PM
Interview ^a	Short-listed candidates in Main	11/12/16, 9 AM to 7 PM

^a Placement talk for Interview Candidates on 11th Dec 2016 from 8 AM to 9 AM

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