

# Engineer Trainees Recruitment

Challenging work opportunities for  
Graduating Engineers

at

## SoCtronics

in core domains

VLSI Digital  
Engineering

VLSI Analog  
Engineering

Embedded System  
Design & Engineering

**ELIGIBILITY : B.Tech./M.Tech./M.S./M.Sc. in Electronics/ Electrical/ Computer Science/ IT/ Instrumentation.  
Final Year Students can also apply.**

**6 months of company sponsored  
knowledge-intensive, industry-oriented full-time training at VEDA IIT  
(One year Internship for PG students includes above training)**

#### About VEDA

- Expert faculty from reputed VLSI Design Houses
- Industry live projects in cutting edge technologies with proven design methodologies
- High End computing facilities - Honoured for excellence in IC Design Education by Sun
- Plenty of tool licenses covering the entire ASIC design flow from world class EDA vendors
- Track record of almost 100% recruitment for MS Graduates in leading "VLSI Companies" since inception
- Partnership with global leaders of technology
- Innovative Teaching Company with Industry and University participation - First of its kind
- Listed among top five institutes in VLSI Engineering by Times of India
- Pioneered in incubating many successful design teams/companies
- Lead organizers of International VLSI Conferences VLSI 2006, 2008, 2012 & 2017

**Apply at [www.vedaiit.org](http://www.vedaiit.org)**

**Registration fee for B.Tech./M.Tech./M.S./M.Sc. pass-outs: Rs. 300/-**

**No fees for meritorious campus students with valid coupon codes.**

**TPOs can write to [hr@vedaiit.com](mailto:hr@vedaiit.com) for coupon codes.**

**Last date for application: 20<sup>th</sup> Feb 2017**

**Placement talk for Interview Candidates on 26<sup>th</sup> Feb 2017 From 8 AM to 9 AM**

Test	Eligibility	Date & Time
<b>Prelim.</b>	Approved Candidates with hall tickets	25/02/17, 9 AM to 10 AM
<b>Main</b>	Short-listed candidates in Prelim	25/02/17, 4 PM to 6 PM
<b>Interview</b>	Short-listed candidates in Main	26/02/17, 9 AM to 7 PM



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