



Learn. Explore. Excel.

RECRUITMENT OF FACULTY

HYDERABAD & AMARAVATI

Asst. Professor

Job Description: Teaching, Research & Training

Eligibility: BE/B.Tech. & ME/M.Tech./MS in relevant branch (ECE/EEE/EIE) with 1st class or equivalent either in BE/B.Tech. or ME/M.Tech./MS.

Salary: As per AICTE norms

Engineer

Job Description: Design of experiments, development & training

Eligibility: BE/B.Tech./M.Sc. in Electronics/Electrical/Computer Science/IT/Instrumentation/MCA with 1st class.

Salary: Negotiable

Final Year Students can also apply.

Core Design Domains

• VLSI Digital Engineering

Logic Design

Digital Design, RTL Implementation, Verification, Synthesis, DFT & Emulation

Physical Design

Physical Placement & Routing, Functional Equivalence, Timing Closure and Design Rule Check

• VLSI Analog Engineering

Analog Design

Analog circuit design, Spice simulation and Verilog A modeling

Custom layout design

Analog/Custom circuit layout, Layout verification

• Embedded System Design

Embedded Software, Firmware, Drivers and Applications Development, HW/Board Design, Silicon Validation & Characterization

• Programming

Program development in

Unix, C, C++, PERL, TCL, Java, Data Structures

The selected candidates will be provided 6 months of knowledge intensive, industry-oriented full-time training.

ABOUT VEDA



Since 1997



130+ Batches



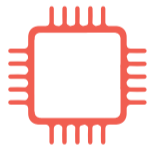
2500+ Students



Well Equipped Labs



Top rated VLSI Institute



Latest EDA Tools



Lead organizers of VLSI Conferences



Consortium Companies



MoU with JNTUH, JNTUK



IMPORTANT DATES

PRELIM.	24/02/18		9:00 AM to 10:00 AM
MAIN#	24/02/18		3:30 PM to 5:30 PM
INTERVIEW	SCHEDULE WILL BE COMMUNICATED THROUGH SMS/EMAIL		

Pre-Placement Talk: 24/02/2018 | 10:15 AM to 10:45 AM

Registration Fee: ₹ 300/-
Application Last Date: 20th Feb 2018*

No fees for campus students who apply on-line with 'valid coupon code'** by 16th Feb 2018

*Application submission can be closed earlier than this whenever the number of applicants exceeds the actual seating capacity at the exam center

**We provide 'Valid coupon codes' to TPO/HODs of different colleges on request; Number will be limited to the seating capacity

#For Short-Listed candidates in Prelim. only



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VEDA IIT, a Unit of The VEDA Educational Society

VEDA IIT, Plot No. 90, Road No. 2, Banjara Hills, Hyderabad 500034

040 - 43929999

+91 9989928276

hr@vedaiit.com

www.vedaiit.org