

ENGINEER TRAINEE RECRUITMENT

Amaravati • Bengaluru • Hyderabad • Noida

Prelim.	Approved candidates with hall tickets	23/02/19, 9 AM to 10 AM
Main	Short-Listed candidates in Prelim.	23/02/19, 3.30 PM to 5.30 PM
Interview	Short-Listed candidates in Main	24/02/19, 8.30 AM to 7 PM [#]

Pre-placement Talk - 23/02/19, 10:15 AM to 10:45 AM

[#]Interview schedule beyond this date will be intimated on that day

VLSI Digital Engineering

- Logic Design
- Physical Design
- Standard Cell Design

VLSI Analog Engineering

- Analog Design
- Custom Layout Design
- Design Characterization

Embedded System Design & Engineering

- Embedded Software Development
- Software Testing
- System Engineering
- Application Software Development

About VEDA

- Expert faculty from reputed VLSI Design Houses/ Institutes.
- Well-equipped Labs with high-end state-of-the-art computing facilities.
- Excellent placement track record in leading Technology Companies since inception.
- Listed among top five institutes in VLSI Engineering by Times of India.
- Innovative Teaching Company with Industry and University participation - First of its kind.
- Pioneered in incubating many successful design teams/ companies.
- Associated with GLOBALFOUNDRIES collaborative university programs, exploring designs for silicon fabrication shuttle.
- Lead organizers of International VLSI & Embedded System Design Conferences VLSI 2006, 2008, 2012 & 2017

ELIGIBILITY

- B.Tech./M.Sc. in Electronics/ Electrical for Digital & Analog Engineering.
- B.Tech./M.Sc. in Electronics/ Electrical/ Computer Science/ IT/Instrumentation for Embedded System Design & Engineering.
- Final year students can also apply.

6 months of company sponsored
knowledge-intensive, industry-oriented full-time training at VEDA IIT

APPLY AT [WWW. VEDAIIT.ORG](http://WWW.VEDAIIT.ORG)

Registration fee for B.Tech /M.Sc Pass-outs: Rs. 300/-
Last date for application : **16th Feb 2019***

No fees for campus students who apply on-line with
'valid coupon code'^{**} by **14th Feb 2019**

*Application submission can be closed earlier than this whenever the number of applicants exceeds the actual seating capacity at the exam center

** VEDA IIT provides 'valid coupon codes' to TPO/HODs of different colleges on request; Number will be limited to the seating capacity