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RECRUITMENT OF FACULTY FOR VEDA IIT, AMARAVATI

Asst. Professor

Job Description: Teaching, Research & Training

Eligibility: BE/B.Tech. & ME/M.Tech./MS in relevant branch (ECE/EEE/EIE) with 1st class or equivalent either in BE/B.Tech. or ME/M.Tech./MS.

Salary: As per AICTE norms

Core Design Domains

• VLSI Digital Engineering

Logic Design

Digital Design, RTL Implementation, Verification, Synthesis, DFT & Emulation

Physical Design

Physical Placement & Routing, Functional Equivalence, Timing Closure and Design Rule Check

• Embedded System Design

Embedded Software, Firmware, Drivers and Applications Development, HW/Board Design, Silicon Validation & Characterization

• Programming

Program development in

Unix, C, C++, PERL, TCL, Java, Data Structures

The selected candidates will be provided 6 months of knowledge intensive, industry-oriented full-time training.

ABOUT VEDA



Since 1997



150+ Batches



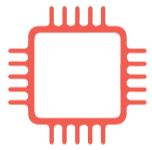
3500+ Students



Well Equipped Labs



Top rated VLSI Institute



Latest EDA Tools



Lead organizers of International VLSI Conferences



Consortium Companies



MoU with JNTUH, JNTUK



APPLY ONLINE - WWW.VEDAIIT.ORG

Last date to Apply: 15th May 2019

Screening Test (On chosen domain): 18th May 2019, 10:00 AM to 1:00 PM

Interview schedule for successful candidates in Screening Test will be informed later.

For more details of the Screening Test, visit <https://vedaiit.org/veda-faculty-recruitment>



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VEDA IIT, a unit of The VEDA Educational Society

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